

KRA2202S

PNP Silicon Transistor

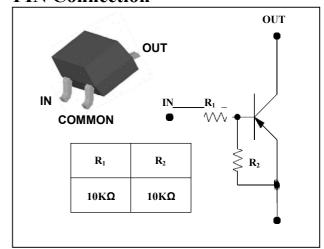
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
KRA2202S	<u>RA2</u> □.	SOT-23	
	1 2	301-23	

①Device Code ②Year&Week Code. Dalian

Absolute Maximum Ratings

(Ta=25 C)

Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	-50	V	
Input voltage	V_{I}	-30, 10	V	
Output current	I_{O}	-100	mA	
Power dissipation	P_D	200	mW	
Junction temperature	T _J	150	°C	
Storage temperature range	T_{stg}	-55 ~ 150	°C	

Electrical Characteristics

(Ta=25 C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	V ₀ =-50V, V _I =0	-	-	-500	nA
DC current gain	Gı	V ₀ =-5V, I ₀ =-10mA	50	80	-	-
Output voltage	V _{O(ON)}	I _O =-10mA, I _I =-0.5mA	ı	-0.1	-0.3	٧
Input voltage (ON)	$V_{I(ON)}$	V_0 =-0.2V, I_0 =-5mA	1	-1.8	-2.4	٧
Input voltage (OFF)	$V_{I(OFF)}$	V ₀ =-5V, I ₀ =-0.1mA	-1.0	-1.2	ı	٧
Transition frequency	* f⊤	V_0 =-10V, I_0 =-5mA, f=1MHz	1	200	ı	MHz
Input current	I_{I}	V_{I} =-5 V , I_{O} =0	ı	ı	-0.88	mA
Input resistor (Input to base)	R ₁	-	7	10	13	K Ω
Input resistor (Base to common)	R ₂	-	7	10	13	K Ω

^{* :} Characteristic of transistor only

Electrical Characteristic Curves

Fig. 1 Pc - Ta

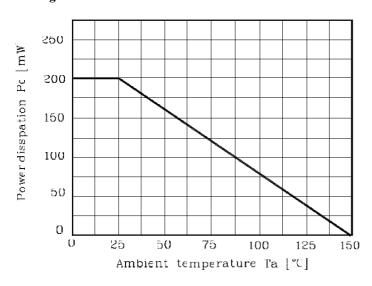


Fig. 2 I_O - $V_{I(ON)}$

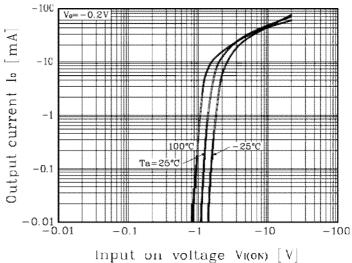


Fig. 3 I_{O} - $V_{I(OFF)}$

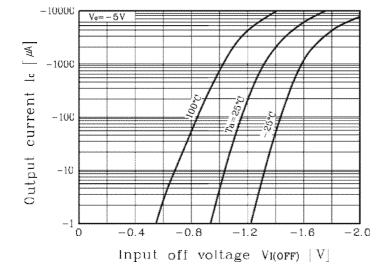
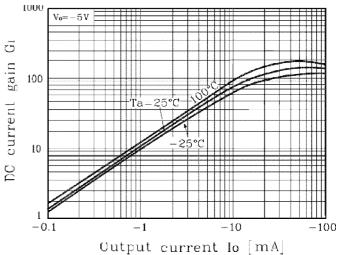
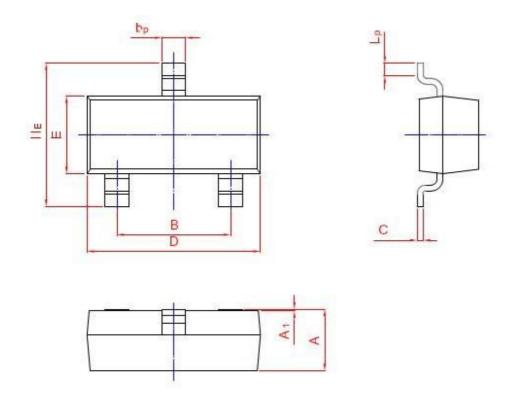


Fig. 4 G_I - I_O

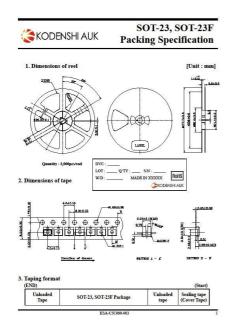


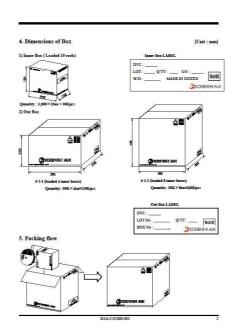
Outline Dimension



UNIT	Α	В	bр	С	D	E	HE	A ₁	Lp
mm	1.40	2.04	0.50	0.19	3.10	1.65	3.00	0.100	0.50
	0.95	1.78	0.35	0.08	2.70	1.20	2.20	0.013	0.20

Packing Specification





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